

2016 IEEE MTT-S Latin America Microwave Conference (LAMC 2016)
Puerto Vallarta, Mexico, Dec. 12-14, 2016

Regular Sessions

RS-4: Power Amplifiers (Mo-8B)

Day: Monday, December 12
Room: Girasol
Time: 16:45–18:05

Chair: J. Apolinar Reynoso-Hernández (CICESE, Ensenada, Mexico)

Co-Chair: Almudena Suárez (University of Cantabria, Santander, Spain)

Time	ID	Paper#	Title / Authors / Affiliations
16:45 to 17:05	Mo-8B-1	112	Broadband Class-E Power Amplifier with High Cold Output Impedance Suitable for Load modulated Dual Branch Amplifiers T. Sharma, P. Aflaki, M. Helaoui, F. Ghannouchi Iradio, Calgary, Canada
17:05 to 17:25	Mo-8B-2	123	20 Gbit/s Ultra-Compact Optical Receiver Front-End with Variable Gain Transimpedance Amplifier in 80 nm CMOS L. Szilagyi, R. Henker, F. Ellinger Technische Universität Dresden, Dresden, Germany
17:25 to 17:45	Mo-8B-3	162	A Fully Integrated 2.6 GHz Cascode Class-E PA in 0.25 μm CMOS Employing New Bias Network for Stacked Transistors M. KreiBig, R. Kostack, J. Pliva, R. Paulo, F. Ellinger Technische Universität Dresden, Dresden, Germany
17:45 to 18:05	Mo-8B-4	178	Improved Envelope Load Pull System for High Power Characterization E. J. Malfavaun González ^{1,2} , M. A. Pulido Gaytán ^{1,2} , J. Urbina Martínez ^{2,1} , B. E. Figueroa Reséndiz ^{2,1} , J. R. Loo Yau ^{2,1} , M. Maya Sánchez ^{1,2} , J. A. Reynoso Hernández ^{1,2} ¹ Centro de Investigación Científica y de Educación Superior de Ensenada (CICESE), Ensenada, Mexico, ² Centro de Investigación y de Estudios Avanzados del I.P.N. Unidad Guadalajara (CINVESTAV-GDL), Zapopan, Mexico